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October 22, 2001

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Washington, D.C. 20231

Re: Our File: TESSERA 3.0-078 DIV  
Appln. No. 09/020,647  
Filing Date: February 9, 1998  
Group Art Unit: 2814

Dear Sir:

We are enclosing herewith copies of the filing receipt and page 1 of a Preliminary Amendment for the above-identified patent application.

Please note that the correct *Title* should read --METHODS OF MAKING COMPLIANT SEMICONDUCTOR CHIP PACKAGES--.

Kindly issue a corrected filing receipt for the above-identified application as soon as possible.

Sincerely yours,

LERNER, DAVID, LITTENBERG,  
KRUMHOLZ & MENTLIK, LLP

ARNOLD H. KRUMHOLZ  
Reg. No. 25,428

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Enclosures  
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1010 Rec'd PTO 25 JAN 2002  
P. 1/25/02

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#29/letter  
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Appln. No. 09/020,647  
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Dear Sir:

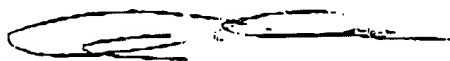
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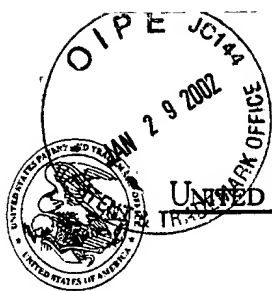
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/020,647	02/09/1998	2814	990	TESSERA3.0- 078 01v	5	11	1

CONFIRMATION NO. 3500

## CORRECTED FILING RECEIPT



\*OC000000006918628\*

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Date Mailed: 10/16/2001

Receipt is acknowledged of a CPA in this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A DIV OF 08/739,303 10/29/1996 PAT 6,211,572  
WHICH CLAIMS BENEFIT OF 60/007,128 10/31/1995

## Foreign Applications

If Required, Foreign Filing License Granted 04/16/1998

CPA filed on: 10/01/2001

Projected Publication Date: 01/24/2002

Non-Publication Request: No

Early Publication Request: No

## Title

SEMICONDUCTOR CHIP PACKAGE WITH FAN-IN LEADS

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No.	Doccode	Number of pages
1	TRTC	1
2	A...	1
3	CLM	2
4	REM	8
5	XT/	1

Total number of pages: 13

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